

# MASTER OF COMPUTER APPLICATIONS-Syllabus

## September'22 Session- Semester- I

Semester 1 (S3)

Program	Course Code	Course Name	Credit	Sem	Th /P /Pro
MCA	OMC001	Fundamentals of Computers and Programming	-	1	Th
MCA	OMC101	Object-Oriented Programming using C++	3	1	Th
MCA	OMC102	Data Structures	3	1	Th
MCA	OMC103	Discrete Mathematics	3	1	Th
MCA	OMC104	Operating Systems	3	1	Th
MCA	OMC105	Digital Logic and Computer Organization	3	1	Th
MCA	OMC121	C++ Laboratory	2	1	P
MCA	OMC122	Data Structures Laboratory	2	1	P

## Fundamentals of Computers and Programming

<b>Course Code: OMC 001</b>	<b>Course Title: Fundamentals of Computers and Programming</b>
-----------------------------	--

**Course Objectives:**

1. **To differentiate between the varying hardware technologies used over the generations of the computer**
2. **To explain how functional units contribute to a computer's successful operation**
3. **To identify the software and hardware components of the computer**
4. To describe the role of OS in running software programs
5. To understand the fundamentals of C programming and write re-usable code using C functions
6. To demonstrate operations such as sorting, searching using arrays
7. To identify an appropriate C programming construct to solve a given problem

--	--

Sr. No	Units	Unit Objectives
1	<p><b>Unit 1: Origin of Computer and its functioning</b></p> <ul style="list-style-type: none"> <li>• Definition of a computer</li> <li>• Characteristics of a computer</li> <li>• Capabilities of a computer</li> <li>• Components of a computer</li> <li>• Functional units of a computer</li> <li>• Evolution</li> <li>• Generations of a computer</li> <li>• Types of computer based on                             <ul style="list-style-type: none"> <li>○ Usage</li> <li>○ Capability</li> <li>○ Working</li> </ul> </li> </ul>	<p>Students will be able to understand the evolution, capabilities and limitations of the computer.</p>

	<ul style="list-style-type: none"> <li>• Applications in various fields</li> <li>• Limitations</li> </ul>	
2	<p><b>Unit 2: I/O devices and Memory System</b></p> <ul style="list-style-type: none"> <li>• Input devices such as <ul style="list-style-type: none"> <li>○ Keyboard</li> <li>○ Mouse</li> <li>○ Joystick</li> <li>○ Scanner</li> <li>○ Microphone</li> <li>○ Webcam</li> </ul> </li> <li>• Output devices such as <ul style="list-style-type: none"> <li>○ Monitor &amp; its types</li> <li>○ Printer &amp; its types</li> <li>○ Speaker</li> <li>○ Plotter</li> <li>○ Projector</li> <li>○ Sound card</li> </ul> </li> <li>• Types of memory <ul style="list-style-type: none"> <li>○ Primary (RAM, ROM)</li> <li>○ Secondary (Hard disk)</li> </ul> </li> <li>• Memory hierarchy</li> <li>• Storage devices such as <ul style="list-style-type: none"> <li>○ Magnetic disk tape</li> <li>○ Hard disk</li> <li>○ Floppy disk</li> </ul> </li> <li>• Bit, Byte, KB, MB, GB and their relation</li> </ul>	<p>Students shall be able to discuss various input and output devices for data communication between the computer and user. Also learn different types of storage devices and their characteristics.</p>
3	<p><b>Unit 3: Computer software concepts (Operating System (OS) &amp; Programming language)</b></p> <ul style="list-style-type: none"> <li>• Introduction to Software</li> <li>• Types of Software <ul style="list-style-type: none"> <li>○ System Software</li> </ul> </li> </ul>	<p>Students shall be able to differentiate between the two types of computer software, list the types of OS and identify the two</p>

	<ul style="list-style-type: none"> <li>○ Application Software</li> <li>● Concept of free &amp; proprietary software</li> <li>● Definition of OS</li> <li>● Need and functions of OS</li> <li>● Types of OS</li> <li>● Definition of a Programming language</li> <li>● Types of Programming languages</li> <li>● Low-level languages</li> <li>● High-level languages</li> <li>● Translators <ul style="list-style-type: none"> <li>○ Assembler</li> <li>○ Compiler</li> <li>○ Interpreter</li> </ul> </li> </ul>	main categories of programming languages.
4	<p><b>Unit 4: Algorithms</b></p> <ul style="list-style-type: none"> <li>● Definition of Algorithm</li> <li>● Properties of an Algorithm</li> <li>● Examples of Algorithm</li> </ul>	Students shall be able to design an algorithm for a given problem.
5	<p><b>Unit 5: Flowcharts</b></p> <ul style="list-style-type: none"> <li>● Definition of Flowchart</li> <li>● Advantages of flowchart</li> <li>● Flowchart symbols</li> <li>● Examples of flowchart (conditional statements, loops etc..)</li> </ul>	Students shall be able to familiarize the flowchart symbols and design a flowchart for a given problem.
6	<p><b>Unit 6: Introduction to C Programming language</b></p> <ul style="list-style-type: none"> <li>● Characteristics of C</li> <li>● Structure of a C program</li> <li>● Life cycle of a C program</li> <li>● First C program – “Hello World”</li> <li>● Commands to execute a C program</li> </ul>	Students shall be able to learn the basic structure and elements of a C programming language.

	<ul style="list-style-type: none"> <li>• C Fundamentals <ul style="list-style-type: none"> <li>○ C Character set</li> <li>○ Constants</li> <li>○ Variables</li> <li>○ Identifiers</li> <li>○ Keywords</li> <li>○ Escape sequences</li> <li>○ Data types</li> </ul> </li> <li>• Macros</li> </ul>	
7	<p><b>Unit 7: C I/O Functions</b></p> <ul style="list-style-type: none"> <li>• Types of I/O functions <ul style="list-style-type: none"> <li>○ Unformatted</li> <li>○ Formatted</li> </ul> </li> <li>• Unformatted I/O functions <ul style="list-style-type: none"> <li>○ getchar()</li> <li>○ putchar()</li> <li>○ gets()</li> <li>○ puts()</li> <li>○ putch()</li> </ul> </li> <li>• Formatted I/O functions <ul style="list-style-type: none"> <li>○ Format specifiers</li> <li>○ scanf()</li> <li>○ printf()</li> </ul> </li> </ul>	<p>Students shall be able to explain Read/Write operations on data using different I/O functions.</p>
8	<p><b>Unit 8: Operators and Expressions in C</b></p> <ul style="list-style-type: none"> <li>• Arithmetic operators</li> <li>• Relational operators</li> <li>• Logical operators</li> <li>• Assignment operators</li> <li>• Bitwise operators</li> <li>• Increment and Decrement operators</li> <li>• Conditional operator</li> </ul>	<p>Students shall be able to evaluate an expression comprising different types of operators.</p>

	<ul style="list-style-type: none"> <li>• Precedence and Associativity</li> <li>• Evaluation of expressions</li> </ul>	
9	<p><b>Unit 9: Control statements in C</b></p> <ul style="list-style-type: none"> <li>• Branching statements <ul style="list-style-type: none"> <li>○ If statement</li> <li>○ If then else statement</li> <li>○ Nested if-else</li> <li>○ Else-if ladder</li> <li>○ switch</li> </ul> </li> <li>• Looping constructs <ul style="list-style-type: none"> <li>○ for</li> <li>○ while</li> <li>○ do while</li> </ul> </li> <li>• Jump statements <ul style="list-style-type: none"> <li>○ break</li> <li>○ continue</li> <li>○ goto</li> <li>○ return</li> </ul> </li> </ul>	<p>Students shall be able to understand the concepts of branching, looping and how to use it in a programming language.</p>
10	<p><b>Unit 10: Arrays and Strings</b></p> <ul style="list-style-type: none"> <li>• Single dimensional array <ul style="list-style-type: none"> <li>○ Array declaration</li> <li>○ Accessing elements of an array</li> <li>○ Initialization</li> <li>○ Array operations (insert, delete, sort and search)</li> </ul> </li> <li>• Two-dimensional arrays <ul style="list-style-type: none"> <li>○ Declaration of a 2D array</li> <li>○ Initialization</li> <li>○ Operations on Matrices (addition, product, transpose)</li> </ul> </li> <li>• Strings</li> </ul>	<p>Students shall be able to declare and define an array to store homogeneous data. Also demonstrate the use of string and string handling functions</p>

	<ul style="list-style-type: none"> <li>○ Declaration of strings</li> <li>○ Initialization</li> <li>○ Input and Output of strings</li> <li>○ Formatting strings</li> <li>○ Edit set conversion code</li> <li>○ String handling functions</li> </ul>	
11	<p><b>Unit 11: Functions and Storage classes in C</b></p> <ul style="list-style-type: none"> <li>● Advantages of functions</li> <li>● Library functions</li> <li>● User-defined functions <ul style="list-style-type: none"> <li>○ Function declaration or prototype</li> <li>○ Function definition</li> <li>○ Function call</li> </ul> </li> <li>● Return statement</li> <li>● Types of functions <ul style="list-style-type: none"> <li>○ Function with no arguments and no return value</li> <li>○ Function with no arguments but with a return value</li> <li>○ Function with arguments but no return value</li> <li>○ Function with arguments and return value</li> </ul> </li> <li>● Call by value and call by reference</li> <li>● Passing array to a function</li> <li>● Recursion <ul style="list-style-type: none"> <li>○ Advantages &amp; disadvantages</li> <li>○ Iteration v/s Recursion</li> <li>○ Examples (Factorial, Fibonacci series...)</li> </ul> </li> <li>● Storage classes <ul style="list-style-type: none"> <li>○ Automatic</li> </ul> </li> </ul>	<p>Students shall be able to demonstrate different ways of passing parameters to a function and return a value from a function.</p>

	<ul style="list-style-type: none"> <li>○ Static</li> <li>○ Register</li> <li>○ External</li> <li>○ Scope of variables with different storage classes</li> </ul>	
12	<p><b>Unit 12: Structures and Unions</b></p> <ul style="list-style-type: none"> <li>• Need of structures</li> <li>• Defining a structure</li> <li>• Declaration of structure variables</li> <li>• Initialization of structure variables</li> <li>• Accessing structure members</li> <li>• Assignment of structure variables</li> <li>• Size of a structure</li> <li>• Array of structures</li> <li>• Structure with arrays</li> <li>• Nested structure</li> <li>• Structures and functions</li> <li>• Self-referential structures</li> <li>• Typedef keyword</li> <li>• Bitfields</li> <li>• Defining a union</li> <li>• Declaration of union variables</li> <li>• Initialization and access of union variables</li> <li>• Size of a union</li> <li>• Nested unions <ul style="list-style-type: none"> <li>○ Union inside union</li> <li>○ Structure inside union</li> <li>○ Union inside structure</li> </ul> </li> <li>• Difference between Structure and Union</li> </ul>	<p>Students shall be able to explain how to use structures and unions to store heterogeneous data in a program.</p>



13	<p><b>Unit 13: C Pointers</b></p> <ul style="list-style-type: none"> <li>• Need of Pointers</li> <li>• Pointer variables <ul style="list-style-type: none"> <li>○ Declaration</li> <li>○ Assigning address to pointer variables</li> <li>○ Dereferencing pointer variables</li> </ul> </li> <li>• Pointer arithmetic</li> <li>• Pointer comparisons</li> <li>• Chain of Pointers</li> <li>• Pointers and Array</li> <li>• Pointers and String</li> <li>• Pointers and Function</li> <li>• Array of Pointers</li> <li>• Void pointers</li> <li>• Null pointers</li> <li>• Dangling pointer</li> <li>• Dynamic memory allocation <ul style="list-style-type: none"> <li>○ malloc()</li> <li>○ calloc()</li> <li>○ realloc()</li> <li>○ free()</li> </ul> </li> </ul>	<p>Students shall be able to discuss on how to declare pointers and use them for dynamic memory allocation. Also understand the pointer arithmetic with arrays.</p>
14	<p><b>Unit 14: File management in C</b></p> <ul style="list-style-type: none"> <li>• Types of files</li> <li>• File modes</li> <li>• Open a file</li> <li>• Close a file</li> <li>• End Of File (EOF)</li> <li>• Character I/O functions <ul style="list-style-type: none"> <li>○ fputc()</li> <li>○ fgetc()</li> </ul> </li> </ul>	<p>Students shall be able to discuss how to open, close a file. Read/write a character, string, integer and a block from/to a file using file handling functions.</p>

	<ul style="list-style-type: none"> <li>• Integer I/O functions <ul style="list-style-type: none"> <li>○ putw()</li> <li>○ getw()</li> </ul> </li> <li>• String I/O functions <ul style="list-style-type: none"> <li>○ fputs()</li> <li>○ fgets()</li> </ul> </li> <li>• Formatted I/O functions <ul style="list-style-type: none"> <li>○ fprintf()</li> <li>○ fscanf()</li> </ul> </li> <li>• Block Read/Write functions <ul style="list-style-type: none"> <li>○ fwrite()</li> <li>○ fread()</li> </ul> </li> <li>• Random access to a file <ul style="list-style-type: none"> <li>○ fseek()</li> <li>○ ftell()</li> <li>○ rewind()</li> </ul> </li> <li>• Error handling in files</li> <li>• Command line arguments</li> </ul>	
--	--	--

**Text Book(s)**

1. Norton & Peter, "Introduction to Computers", 6<sup>th</sup> Edition, McGraw-Hill, 2009
2. Leon, Alexis & Leon, Mathews, "Introduction to Computers", 1<sup>st</sup> Edition, Vikas Publishing, 2009
3. Yashavant Kanetkar, "Let Us C", 14<sup>th</sup> Edition, BPB Publication, 2016
4. E. Balagurusamy, "Programming in ANSI C", 6<sup>th</sup> Edition, McGraw-Hill, 2015

**Reference Book(s)**

1. P K.Sinha & Priti Sinha, "Computer Fundamentals", 8<sup>th</sup> Edition, BPB, 2004
2. Brian W Kernighan & Dennis M Ritchie, "The C Programming Language", 2<sup>nd</sup> Edition, Prentice Hall, 1988

## Object-Oriented Programming Using C++

<b>Course Code: OMC 101</b>		<b>Course Title: Object-Oriented Programming Using C++</b>
<b>Course Objectives:</b> <ol style="list-style-type: none"> <li>1. Understand the difference between the top-down and bottom-up approach</li> <li>2. Describe the object-oriented programming approach in connection with C++</li> <li>3. Apply the concepts of object-oriented programming</li> <li>4. Illustrate the process of data file manipulations using C++</li> <li>5. Apply virtual and pure virtual function &amp; complex programming situations</li> </ol>		
Sr. No	Units	Unit Objectives
1	<b>Unit 1: Object Oriented Programming</b> <ul style="list-style-type: none"> <li>• Procedure Oriented vs. Object Oriented Programming,</li> <li>• Object Oriented Programming Concepts,</li> <li>• Benefits of Object oriented programming,</li> <li>• Object Oriented Languages.</li> </ul>	Students will learn about the significance of Object Oriented Programming Languages.
2	<b>Unit 2: Introduction to C++</b> <ul style="list-style-type: none"> <li>▪ Structure of a C++ program</li> <li>▪ Data Types</li> <li>▪ Operators and Control Structures.</li> </ul>	Students will be able to learn the representation of Data in C++ and basic programming structures.
3	<b>Unit 3: Objects and Classes</b> <ul style="list-style-type: none"> <li>▪ Defining Classes</li> <li>▪ Encapsulation</li> <li>▪ Instantiating Objects</li> <li>▪ Member Functions</li> </ul>	Students will be able to Define Classes, Hiding Data, and Binding Data with Functions.

	<ul style="list-style-type: none"> <li>▪ Accessibility labels</li> <li>▪ Static Members</li> </ul>	
4	<b>Unit 4: Constructors and Destructors</b> <ul style="list-style-type: none"> <li>▪ Purpose of Constructors</li> <li>▪ Default Constructor</li> <li>▪ Parameterized Constructors</li> <li>▪ Copy Constructor, Destructor</li> <li>▪ Memory Management</li> </ul>	Students will learn to initialize the objects and deallocate the memory whenever an object is destroyed.
5	<b>Unit 5: Inheritance</b> <ul style="list-style-type: none"> <li>▪ Concept of Reusability</li> <li>▪ Types of Inheritance</li> <li>▪ Single and Multiple Inheritance</li> <li>▪ Multilevel Inheritance</li> </ul>	Students will learn to reuse, extend or modify the attributes and behaviors which are defined in other class/classes.
6	<b>Unit 6: Operator Overloading</b> <ul style="list-style-type: none"> <li>▪ Function and Operator Overloading</li> <li>▪ Overloading Unary and Binary Operators</li> </ul>	Students will learn to use operators to work for user-defined classes.
7	<b>Unit 7: Polymorphism and Virtual Function</b> <ul style="list-style-type: none"> <li>▪ Abstract Class</li> <li>▪ Function Overriding</li> <li>▪ Dynamic Binding</li> <li>▪ Pure Virtual Functions</li> </ul>	Students will be able to write a function that can evaluate or be applied to values of different types and redefine a function of the base class in the derived class.
8	<b>Unit 8: Streams and Files Stream Classes</b> <ul style="list-style-type: none"> <li>▪ Types of I/O</li> <li>▪ Formatting Outputs</li> <li>▪ File Pointers</li> <li>▪ Buffer</li> </ul>	Students will learn to access data from files and format the data.
9	<b>Unit 9: Templates and STL</b>	Students will be able to create a single function or single

	<ul style="list-style-type: none"> <li>▪ Function and Class Templates</li> <li>▪ Use of Templates</li> <li>▪ Standard Template Library</li> </ul>	class to work with different data types using templates
10	<b>Unit 10: Exception Handling</b> <ul style="list-style-type: none"> <li>▪ Exceptions in C++ Programs</li> <li>▪ Try and Catch Expressions</li> <li>▪ Exceptions with argument</li> </ul>	Students will learn to handle run-time anomalies or abnormal conditions that a program encounters during its execution.
<b>Text Book(s)</b> <ol style="list-style-type: none"> <li>1. E. Balagurusamy, "Object Oriented Programming with C++", 4<sup>th</sup> edition, Tata McGraw Education Hill, 2011.</li> <li>2. Herbert Schildt – The Complete Reference C++, Fourth Edition, Tata McGraw Hill, 2003</li> </ol> <b>Reference Book(s)</b> <ol style="list-style-type: none"> <li>1. Robert Lafore - Object Oriented Programming with C++, Fourth Edition, Pearson Education India, 2008</li> <li>2. Stephen Prata, "C++ Primer Plus: Teach Yourself Object Oriented Programming", 2<sup>nd</sup> Edition, Waite Group, 1995</li> </ol>		

## Data Structures

<b>Course Code: OMC 102</b>		<b>Course Title: Data Structures</b>
<p><b>Course Objectives:</b></p> <ol style="list-style-type: none"> <li>1. Describe the concept of Data Structures and assess how the choice of data structures impacts the performance of programs</li> <li>2. Compare and contrast the merits and demerits of various data structures in terms of time and memory complexity.</li> <li>3. Identify and propose appropriate data structures for providing the solution to real-world problems.</li> <li>4. Implement operations like searching, insertion, deletion, traversing mechanism, etc. on various data structures</li> <li>5. Be familiar with advanced data structures such as balanced search trees, hash tables, AVL trees, priority queues, ADT, etc.</li> <li>6. To augment the merits of particular data structures on other data structures to develop innovation in the subject of study.</li> </ol>		
Sr. No	Units	Unit Objectives
1	<p><b>Unit 1: Introduction</b></p> <ul style="list-style-type: none"> <li>• Basic Terminology</li> <li>• Pointer and dynamic memory allocation</li> <li>• Elementary Data Organization</li> <li>• Algorithm Complexity and Time-Space trade-offs</li> </ul>	Students will learn Dynamic Memory Management, Data Organisation, and Efficiency.
2	<p><b>Unit 2: Arrays</b></p> <ul style="list-style-type: none"> <li>▪ Array Definition</li> <li>▪ Representation and Analysis</li> <li>▪ Single and Multidimensional Arrays</li> <li>▪ Sparse Matrices</li> </ul>	Students will be able to learn and use Single and Multidimensional Arrays.
3	<p><b>Unit 3: Recursion</b></p> <ul style="list-style-type: none"> <li>▪ Definition</li> <li>▪ Tail recursion</li> </ul>	Students will be able to develop programs using Recursion.

4	<b>Unit 4: Stacks</b> <ul style="list-style-type: none"> <li>▪ Array Representation of stack</li> <li>▪ Linked Representation of Stack</li> <li>▪ Infix, Prefix, and Postfix Expressions</li> </ul>	Students will be able to develop programs using Stacks for various applications.
5	<b>Unit 5: Queues</b> <ul style="list-style-type: none"> <li>▪ Array and linked representation and of queues</li> <li>▪ Circular queue</li> <li>▪ D-queue</li> <li>▪ Priority Queue</li> </ul>	Students will be able to learn different types of Queues and use them to develop programs for various applications.
6	<b>Unit 6: Linked list</b> <ul style="list-style-type: none"> <li>▪ Representation of Singly Linked Lists</li> <li>▪ Two-way Header List</li> <li>▪ Doubly linked list</li> <li>▪ Generalized linked list</li> </ul>	Students will be able to represent data using Linked Lists and develop programs for various applications.
7	<b>Unit 7: Trees</b> <ul style="list-style-type: none"> <li>▪ Binary Trees</li> <li>▪ Binary Search tree</li> <li>▪ Algebraic Expressions</li> <li>▪ Complete Binary Tree</li> <li>▪ Extended Binary Trees</li> <li>▪ Threaded Binary trees</li> <li>▪ AVL Tree</li> <li>▪ Huffman algorithm &amp; Huffman tree</li> </ul>	Students will be able to represent data using Trees, learn to balance the trees, and code the information using Huffman Trees.
8	<b>Unit 8: Searching and Hashing</b> <ul style="list-style-type: none"> <li>▪ Sequential search</li> <li>▪ Binary search</li> <li>▪ Hash Table</li> <li>▪ Hash Functions</li> <li>▪ Collision Resolution Strategies</li> </ul>	Students will be able to use different searching techniques.

9	<b>Unit 9: Sorting</b> <ul style="list-style-type: none"> <li>▪ Insertion Sort</li> <li>▪ Bubble Sorting</li> <li>▪ Quick Sort</li> <li>▪ Two Way Merge Sort</li> <li>▪ Heap Sort</li> </ul>	Students will be able to use different sorting methods to arrange the data.
10	<b>Unit 10: Graphs</b> <ul style="list-style-type: none"> <li>▪ BFS</li> <li>▪ DFS</li> <li>▪ Spanning tree</li> <li>▪ Minimum Spanning tree, Kruskal's Algorithm, Prim's Algorithm</li> <li>▪ Applications of graph</li> </ul>	Students will be able to represent the data using Graphs and develop applications.
11	<b>Unit 11: File Structures</b> <ul style="list-style-type: none"> <li>▪ Physical Storage Media</li> <li>▪ File Organization, Organization of records into Blocks</li> <li>▪ Sequential Files</li> <li>▪ Indexing</li> <li>▪ Primary indices</li> <li>▪ Secondary indices</li> <li>▪ B+ Tree index Files</li> <li>▪ B Tree index Files</li> <li>▪ Indexing and Hashing Comparisons</li> </ul>	Students will be able to learn different techniques of Data Organisation.

**Text Book(s)**

1. Horowitz and Sahani, "fundamental of data structures", Galgotia Publishers, 1999
2. A.M Tenenbaum et al, "data structures and program design in C", 2<sup>nd</sup> edition, Pearson education, 2006

**Reference Book(s)**

1. Lipschutz, "data structures", 1<sup>st</sup> Edition, Tata McGraw Hill Education,2014
2. R. Kruse et al, "data structures and program design in C", 2<sup>nd</sup> Edition, Pearson India,2006



3. K Loudon, "Mastering algorithm with C", O'Reilly publishers, 1999

## Discrete Mathematics

<b>Course Code: OMC 103</b>		<b>Course Title: Discrete Mathematics</b>
<b>Course Objectives:</b> <ol style="list-style-type: none"> <li>1. Apply the fundamentals of set theory for the given problem.</li> <li>2. Solve the given problem by applying Logical Reasoning.</li> <li>3. To prove the correctness of programs using Logical Reasoning.</li> <li>4. Apply techniques of counting in the analysis of algorithms.</li> <li>5. Apply Binary relations to establish relationships between different entities.</li> </ol>		
Sr. No	Units	Unit Objectives
1	<b>Unit 1: Fundamentals</b> <ul style="list-style-type: none"> <li>▪ Sets and Subsets</li> <li>▪ Operations on sets                             <ul style="list-style-type: none"> <li>○ Algebraic properties of set operations</li> <li>○ The Addition principle</li> <li>○ The Addition Principle for Disjoint sets</li> </ul> </li> <li>▪ Sequences                             <ul style="list-style-type: none"> <li>○ Finite and Infinite sequences</li> <li>○ Characteristic Functions</li> <li>○ Computer Representation of sets and subsets</li> <li>○ Strings and Regular Expressions</li> </ul> </li> </ul>	Students will be able to understand the background to begin the exploration of Mathematical Structures.
2	<b>Unit 2: Finite Automata</b> <ul style="list-style-type: none"> <li>▪ Deterministic Finite Automata (DFA)</li> <li>▪ Non Deterministic Finite Automata (NFA)</li> </ul>	Students will be able to design the simplest abstract machines to recognize patterns
3	<b>Unit 3: Logic</b> <ul style="list-style-type: none"> <li>• Propositions and Logical Operations                             <ul style="list-style-type: none"> <li>○ Logical Connectives and Compound Statements</li> </ul> </li> </ul>	Students learn the methods of reasoning and use them to prove theorems in mathematics and prove the

	<ul style="list-style-type: none"> <li>○ Quantifiers</li> <li>● Conditional Statements</li> <li>● Methods of Proof</li> <li>● Mathematical Induction</li> </ul>	correctness of algorithms in computer science.
4	<b>Unit 4: Counting</b> <ul style="list-style-type: none"> <li>▪ Permutations</li> <li>▪ Combinations</li> <li>● Pigeonhole Principle</li> </ul>	Students will be able to apply the techniques of counting in the analysis of algorithms.
5	<b>Unit 5: Relations and Digraphs</b> <ul style="list-style-type: none"> <li>▪ Product Sets and Partitions</li> <li>▪ Relations and Digraphs <ul style="list-style-type: none"> <li>○ Relations</li> <li>○ Sets Arising from Relations</li> <li>○ The Matrix of a Relation</li> <li>○ Digraphs</li> <li>○ Paths in Digraphs</li> </ul> </li> <li>▪ Properties of Relations <ul style="list-style-type: none"> <li>○ Reflexive and Irreflexive Relations</li> <li>○ Symmetric, Asymmetric, and Antisymmetric Relations</li> <li>○ Transitive Relations</li> </ul> </li> <li>▪ Equivalence Relations <ul style="list-style-type: none"> <li>○ Equivalence Relations and Partitions</li> </ul> </li> </ul>	Students will be able to learn and develop binary relations among different entities.
6	<b>Unit 6: Functions</b> <ul style="list-style-type: none"> <li>▪ Functions <ul style="list-style-type: none"> <li>○ Special Types of Functions</li> <li>○ Invertible Functions</li> </ul> </li> <li>▪ Functions for Computer Science <ul style="list-style-type: none"> <li>○ Hashing Functions</li> </ul> </li> </ul>	Students will be able to understand the concept of functions and use them as basic building blocks in real-life applications.

7	<p><b>Unit 7: Graph Theory</b></p> <ul style="list-style-type: none"> <li>▪ Graphs <ul style="list-style-type: none"> <li>○ Graphs</li> <li>○ Subgraphs and Quotient Graphs</li> </ul> </li> <li>▪ Euler paths and Circuits</li> <li>▪ Hamiltonian Paths and Circuits</li> <li>▪ Coloring of Graphs <ul style="list-style-type: none"> <li>○ Chromatic Polynomials</li> </ul> </li> </ul>	Students will be able to develop real-time applications.
8	<p><b>Unit 8: Order Relations and Structures</b></p> <ul style="list-style-type: none"> <li>▪ Partially Ordered Sets <ul style="list-style-type: none"> <li>○ Hasse Diagrams</li> <li>○ Topological Sorting</li> <li>○ Isomorphism</li> </ul> </li> </ul>	Students will be able to construct the logical representation of computer circuits.
9	<p><b>Unit 9: Trees</b></p> <ul style="list-style-type: none"> <li>▪ Trees</li> <li>▪ Labelled Trees <ul style="list-style-type: none"> <li>○ Computer Representation of Binary Positional Trees</li> </ul> </li> <li>▪ Tree Searching <ul style="list-style-type: none"> <li>○ Searching General Trees</li> <li>○ Pseudocode Versions</li> </ul> </li> </ul>	Students will be able to develop applications using tree searching methods.
10	<p><b>Unit 10: Semigroups and Groups</b></p> <ul style="list-style-type: none"> <li>▪ Binary Operations <ul style="list-style-type: none"> <li>○ Tables</li> <li>○ Properties of Binary Operations</li> </ul> </li> <li>▪ Semigroups <ul style="list-style-type: none"> <li>○ Isomorphism and Homomorphism</li> </ul> </li> <li>▪ Products and Quotients of Semigroups</li> <li>▪ Groups.</li> </ul>	Students will be able to apply the basics of group theory in coding theory.

**Text Book(s):**

1. Kollman, Busby and Ross, "Discrete Mathematical Structures", 3rd edition, Pearson Publishers, 2015
2. Gremaldi, Ramana, "Discrete and Combinatorial Mathematics", 5th edition, Pearson Publishers, 2006

**Reference Book(s):**

1. Susanna Epp, "Discrete Mathematics with Applications", 4<sup>th</sup> Edition, Wadsworth Publishing Co Inc, 2010
2. Kenneth Rosen, "Discrete Mathematics and its Applications", 8<sup>th</sup> Edition, McGraw Hill, 2021

## Operating Systems

<b>Course Code: OMC 104</b>		<b>Course Title: Operating Systems</b>
<p><b>Course Objectives:</b></p> <p>Students will learn various services provided by operating systems and the functions performed by the different components of operating systems.</p>		
Sr. No	Units	Unit Objectives
1	<p><b>Unit 1: Introduction</b></p> <ul style="list-style-type: none"> <li>• Definition <ul style="list-style-type: none"> <li>○ What is an OS</li> <li>○ Goals of OS</li> </ul> </li> <li>• Components of Computer System</li> <li>• Types of operating systems <ul style="list-style-type: none"> <li>○ Batch Operating System</li> <li>○ Multiprogramming Operating System</li> <li>○ Multitasking / Time-Sharing Operating System</li> <li>○ Real Time Operating System</li> <li>○ Distributed Operating System</li> <li>○ Parallel Operating System</li> <li>○ Embedded Operating System</li> </ul> </li> <li>• Operating system components <ul style="list-style-type: none"> <li>○ Process Management</li> <li>○ Memory Management</li> <li>○ File Management</li> <li>○ Secondary Storage Management</li> <li>○ Networking</li> <li>○ Protection System</li> </ul> </li> <li>• Services of Operating System <ul style="list-style-type: none"> <li>○ Program execution</li> <li>○ I/O/ operation</li> </ul> </li> </ul>	<p>Students will be able to define the operating system and identify different types of operating systems, various components, and services provided by operating systems.</p>

	<ul style="list-style-type: none"> <li>○ File system manipulation</li> <li>○ Communication</li> <li>○ Error detection</li> <li>○ Resource allocation</li> <li>○ Accounting</li> <li>○ Protection</li> <li>○ User interface</li> <li>● System Calls <ul style="list-style-type: none"> <li>○ Definition explanation in brief</li> <li>○ Introduction to Threads</li> </ul> </li> </ul>	
2	<p><b>Unit 2: Process Concept</b></p> <ul style="list-style-type: none"> <li>● Process definition</li> <li>● Process Control Block</li> <li>● Process States</li> <li>● Scheduling queues</li> <li>● Schedulers <ul style="list-style-type: none"> <li>○ Long term schedulers</li> <li>○ CPU schedulers</li> </ul> </li> <li>● Context switch</li> <li>● CPU scheduling criteria</li> <li>● Scheduling algorithms <ul style="list-style-type: none"> <li>○ FCFS</li> <li>○ SJF</li> <li>○ RR</li> <li>○ Priority Scheduling</li> </ul> </li> </ul>	Students will be able to demonstrate different states of a process and evaluate CPU scheduling algorithm performance.
3	<p><b>Unit 3: Inter-process communication</b></p> <ul style="list-style-type: none"> <li>● Communication <ul style="list-style-type: none"> <li>○ Shared memory</li> <li>○ Message passing</li> </ul> </li> <li>● Process Synchronization</li> <li>● Background</li> </ul>	Students will be able to explain communication among processes and examine process synchronization

	<ul style="list-style-type: none"> <li>• The Critical-Section problem <ul style="list-style-type: none"> <li>○ Two process solutions (general)</li> </ul> </li> <li>• Semaphores <ul style="list-style-type: none"> <li>○ Types</li> </ul> </li> <li>• Classical problems of synchronization (explanation without algorithm) <ul style="list-style-type: none"> <li>○ Readers' and writers' problem</li> </ul> </li> </ul>	
4	<p><b>Unit 4: Deadlock</b></p> <ul style="list-style-type: none"> <li>• System model</li> <li>• Characterization <ul style="list-style-type: none"> <li>○ Necessary conditions</li> <li>○ Resources allocation graph</li> <li>○ Methods for handling deadlock</li> </ul> </li> <li>• Deadlock prevention</li> <li>• Avoidance and Detection <ul style="list-style-type: none"> <li>○ Safestate</li> </ul> </li> <li>• Bankers algorithm</li> <li>• Recovery from deadlock <ul style="list-style-type: none"> <li>○ Process termination</li> <li>○ Resource preemption</li> </ul> </li> </ul>	Students will be able to compare the deadlock problems and their solutions.
5	<p><b>Unit 5: Memory Management</b></p> <ul style="list-style-type: none"> <li>• Memory management <ul style="list-style-type: none"> <li>○ Overlays</li> </ul> </li> <li>• Logical and Physical Address Space</li> <li>• Swapping</li> <li>• Contiguous allocation <ul style="list-style-type: none"> <li>○ Single partition</li> <li>○ Multiple partitions <ul style="list-style-type: none"> <li>▪ First fit</li> <li>▪ Best fit</li> <li>▪ Worst fit</li> </ul> </li> </ul> </li> </ul>	Students will be able to select a memory management scheme for the specific system.



	<ul style="list-style-type: none"> <li>• Paging <ul style="list-style-type: none"> <li>○ Hardware support</li> <li>○ Paging hardware with TLB</li> </ul> </li> <li>• Segmentation <ul style="list-style-type: none"> <li>○ Definition</li> <li>○ Difference between paging and segmentation</li> </ul> </li> </ul>	
6	<p><b>Unit 6: Virtual Memory</b></p> <ul style="list-style-type: none"> <li>• Virtual Memory Background</li> <li>• Demand paging <ul style="list-style-type: none"> <li>○ Page fault</li> </ul> </li> <li>• Page replacement algorithms <ul style="list-style-type: none"> <li>○ FIFO</li> <li>○ LRU</li> <li>○ Optimal</li> </ul> </li> <li>• Thrashing <ul style="list-style-type: none"> <li>○ Definition</li> </ul> </li> </ul>	Students will be able to evaluate the cost and complexity of demand paging.
7	<p><b>Unit 7: File Systems</b></p> <ul style="list-style-type: none"> <li>• File concept <ul style="list-style-type: none"> <li>○ Attributes</li> <li>○ File operations</li> </ul> </li> <li>• Access methods <ul style="list-style-type: none"> <li>○ Direct access</li> <li>○ Sequential access</li> </ul> </li> <li>• Directory structure <ul style="list-style-type: none"> <li>○ introduction</li> </ul> </li> </ul>	Students will be able to distinguish various aspects of files and directory structure.
8	<p><b>Unit 8: Disk Management, Protection and Security</b></p> <ul style="list-style-type: none"> <li>• Disk structure</li> <li>• Disk scheduling methods. <ul style="list-style-type: none"> <li>○ FCFS (with Example)</li> <li>○ SSTF (introduction)</li> </ul> </li> </ul>	Students will be able to review the disk scheduling algorithms and interpret the protection mechanism

	<ul style="list-style-type: none"><li>○ SCAN</li><li>○ C-SCAN</li><li>○ LOOK</li><li>● Protection and Security<ul style="list-style-type: none"><li>○ Goals of protection</li><li>○ Domain of protection</li><li>○ Access matrix</li></ul></li></ul>	provided by the operating system.
--	--	-----------------------------------

**Text Book(s)**

1. Silberschatz, Galvin” Operating Systems Concept, 7<sup>th</sup> edition, John Wiley and sons

**Reference Book(s)**

1. William Stalling: Operating Systems: Internal and design principles, 7<sup>th</sup> edition  
PHI
2. D M Dhamdhere, “Operating Systems: A Concept-Based Approach” TMH

# Digital Logic and Computer Organization

<b>Programme</b>	Master of Computer Applications
<b>Semester</b>	I
<b>Course Title</b>	Digital Logic and Computer Organization
<b>Course Code</b>	OMC105
<b>Course Credits</b>	3
<b>Course Type</b>	Core Theory Course

## 1. Course Summary

This course aims to provide the learners with a foundation of Digital logic and computer organization. The initial part of this course deals with data representation, basic components of digital systems, and their implementation. Students learn Boolean algebra, logic gates, implementation of combinational and sequential digital circuits using logic gates, and use of k-map for logic expression minimization. The later part discusses computer generations, functional components, their characteristics, performance, and interactions. Students learn addressing modes, input-output, and memory organization. Students gain an understanding of fundamental architectural techniques used to build today's high-performance processors and systems.

## 2. Course Outcomes (COs)

After the successful completion of this course, the student will be able to:

- CO-1.** List types of digital circuits and functional units of a computer [L-1]
- CO-2.** Explain the operation of the arithmetic unit including the algorithms & implementation of fixed-point and floating-point addition, subtraction, multiplication & division [L-2]
- CO-3.** Demonstrate the hierarchical memory system [L-3]
- CO-4.** Analyze the different ways of I/O operations and their effect on performance [L-4]
- CO-5.** Design simple logic circuits using gates, multiplexers, demultiplexers, encoders, and decoders[L-6]

## 3. Course Contents

Sr. No	Units	Unit Outcomes
1	<b>Unit 1: Data Representation</b> <ul style="list-style-type: none"> <li>• Number Systems-Binary, Octal, Decimal, Hexadecimal, Interconversion</li> <li>• Binary Addition and Subtraction</li> <li>• Representation of Negative Numbers-1's Complement, 2's Complement</li> </ul>	After the successful completion of the unit, the learner should be able to: <ol style="list-style-type: none"> <li>1. List the different formats to represent numerical data and convert from one to another.</li> <li>2. Demonstrate 1's-complement and 2's complement representation of negative integers.</li> <li>3. Identify different character codes</li> </ol>

2	<b>Unit 2: Logic Circuits</b> <ul style="list-style-type: none"> <li>• Logic gates</li> <li>• Types of Logic Gates-Basic Gates, Universal gates, Special gates</li> <li>• Boolean Algebra- Use of Boolean Algebra, Identities of Boolean Algebra, DeMorgan's Theorems, Forms of Boolean Expressions, Minterms, and Maxterms</li> <li>• Simplification of Logic Expressions-Introduction to K-maps, Minimization Using K-maps</li> </ul>	<ol style="list-style-type: none"> <li>1. Represent logic gates using logic symbols, truth tables, and logic expressions.</li> <li>2. Explain De Morgan's theorems.</li> <li>3. Simplify logic expressions using laws and identities of Boolean Algebra.</li> <li>4. Apply Karnaugh maps to obtain minimal expressions</li> </ol>
3	<b>Unit 3: Combinational and Sequential Circuits</b> <ul style="list-style-type: none"> <li>• Combinational Circuits-Adders and Subtractors, Decoder, Encoder, Multiplexer, Demultiplexer</li> <li>• Sequential Circuits</li> <li>• Flip-Flops-Types of Flip Flops</li> <li>• Registers</li> <li>• Counters</li> </ul>	<ol style="list-style-type: none"> <li>1. Implement the following circuits: Half adder and Full adder Multiplexer and Demultiplexer Decoder and encoder</li> <li>2. Represent the different flip-flops using truth tables.</li> </ol>
4	<b>Unit 4: The Computer and Generations</b> <ul style="list-style-type: none"> <li>• The von Neumann Computer</li> <li>• Development of Computer Hardware-The First Generation, The Second Generation, The Third Generation, The Fourth Generation, Beyond the Fourth Generation</li> </ul>	<ol style="list-style-type: none"> <li>1. Explain the functional units of the Von Neumann machine</li> <li>2. Explain computer evolution through various generations</li> </ol>
5	<b>Unit 5: Basic Structure of Computers</b> <ul style="list-style-type: none"> <li>• Computer Types</li> <li>• Functional Units</li> <li>• Basic Operational Concepts</li> <li>• Bus structure</li> <li>• Performance</li> <li>• Multiprocessing and Multi computers</li> </ul>	<ol style="list-style-type: none"> <li>1. Explain basic functional units and organization of computer.</li> <li>2. Explain the use of a bus for data transfer.</li> <li>3. Identify the various factors affecting performance.</li> </ol>
6	<b>Unit 6: Machine Instructions and Programs:</b> <ul style="list-style-type: none"> <li>• Memory Locations and Addresses</li> <li>• Memory Operations</li> <li>• Instructions and Instruction sequencing</li> <li>• Addressing Modes.</li> </ul>	<ol style="list-style-type: none"> <li>1. Outline the different concepts related to the Instructions of a computer.</li> <li>2. Explain the instructions sequencing.</li> <li>3. Identify the various addressing modes</li> </ol>
7	<b>Unit 7: Input/Output Organization:</b> <ul style="list-style-type: none"> <li>• Accessing I/O Devices</li> <li>• Interrupts</li> <li>• Enabling and Disabling Interrupts</li> <li>• Handling Multiple Devices</li> <li>• Controlling Device Requests</li> <li>• Direct Memory Access</li> </ul>	<ol style="list-style-type: none"> <li>1. Demonstrate the different ways of accessing I/O devices.</li> <li>2. Explain the interrupt mechanism</li> <li>3. Explain DMA operation</li> </ol>
8	<b>Unit 8: Memory System:</b> <ul style="list-style-type: none"> <li>• Basic Concepts,</li> <li>• Semiconductor RAM Memories,</li> <li>• Read Only Memories,</li> <li>• Speed, Size, and Cost,</li> <li>• Cache Memories</li> <li>• Virtual Memory</li> </ul>	<ol style="list-style-type: none"> <li>1. Identify the different types of memory technology.</li> <li>2. Explain memory and how the memory hierarchy reduces effective memory latency.</li> <li>3. Explain the role of cache memory.</li> </ol>
9	<b>Unit 9: Basic Processing Unit:</b>	<ol style="list-style-type: none"> <li>1. Explain processor organization</li> </ol>

	<ul style="list-style-type: none"> <li>• Some Fundamental Concepts,</li> <li>• Execution of Complete Instruction, Multiple Bus Organization,</li> <li>• Hard-wired Control,</li> <li>• Micro programmed Control</li> </ul>	<ol style="list-style-type: none"> <li>2. Explain multiple bus organizations.</li> <li>3. Differentiate hard-wired control and microprogrammed control</li> </ol>
10	<b>Unit 10: Fundamentals of computer architecture and pipelining</b> <ul style="list-style-type: none"> <li>• VonNeumann vs. Harvard</li> <li>• RISC vs. CISC</li> <li>• Importance of RISC</li> <li>• Pipelining: Basic concepts of pipelining</li> </ul>	<ol style="list-style-type: none"> <li>1. Explain the various architectures</li> <li>2. Highlight the importance of RISC architecture</li> <li>3. Explain the importance of pipelining.</li> </ol>

#### 4. Course Articulation Matrix (CO-PO-PSO Map)

	Programme Outcomes (POs)												Programme Specific Outcomes (PSOs)			
	PO-1	PO-2	PO-3	PO-4	PO-5	PO-6	PO-7	PO-8	PO-9	PO-10	PO-11	PO-12	PSO-1	PSO-2	PSO-3	PSO-4
CO-1	3										2		3	2	2	2
CO-2	3										2		3	2	2	2
CO-3	3	3	1								2		3	2	2	2
CO-4	3	3	2	2							2		3	2	2	2
CO-5	3	3	3	3					1		3		3	3	2	2
3: Very Strong Contribution, 2: Strong Contribution, 1: Moderate Contribution																

#### 5. Course Resources

##### a. Essential Reading

1. Course Self-Learning Material
2. M Morris Mano: Digital Logic and Computer Design, 4th Edition, Pearson Education, 2014.
3. Computer Organization, Carl Hamacher, Zvonko Vranesic, Safwat Zaky, 5th Edition, Tata McGraw Hill, 2017.

##### b. Recommended Reading

1. William Stallings, (2016) *Computer Organization & Architecture*, 10th ed., PHI.
2. Charles H. Roth Jr and Larry L Kinney, (2014) *Fundamentals of Logic Design*. 7th ed., Cengage Learning.

##### c. Magazines and Journals

1. IEEE transaction on Very Large-Scale Integration (VLSI) Systems
2. IEEE transactions on Parallel and Distributed Systems

##### d. Websites

1. <https://www.coursera.org/>

2. <http://nptel.ac.in/>

**e. Other Electronic Resources**

1. <https://ocw.mit.edu/index.htm>
2. Course Video Lectures on ILearn